PHANTOM GRID :: Qloo Agentic AI Hackathon

- Deterministic ASIL-D agent architecture
- Dual-track orchestrator + core engine
- Hard 50 GB VRAM fuse, ZDR wipe to 0.00 MB
- Gate-Before-Ship dual-seal verification

01 System Topology

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flowchart TD



Jack Hu (Solo HITL Architect) | ASIL-D | VRAM <= 50GB | ZDR 0.00 MB

02 Sequence Flow

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sequenceDiagram



03 State Machine

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stateDiagram-v2



Jack Hu (Solo HITL Architect) | ASIL-D | VRAM <= 50GB | ZDR 0.00 MB

04 Security Boundary

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flowchart LR



Jack Hu (Solo HITL Architect) | ASIL-D | VRAM <= 50GB | ZDR 0.00 MB

Table 1 :: Resource Defense Matrix

Resource / Risk	Defense Mechanism	Threshold	Verification
GPU VRAM	Hard fuse in orchestrator	<= 50 GB	Chaos run peak 41.8 GB
Residual memory	ZDR double wipe 0xAA -> 0x00	0.00 MB	Post-run scan
Untrusted input	Schema + policy gate	100% validated	1,500 chaos iterations
Runaway loop	Watchdog + step budget	<= 64 steps	Loop injection test
Delivery gate	Gate-Before-Ship dual seal	all green	Qloo Agentic AI Hackathon gate suite